

EXPRESS MAIL LABEL NO:
EV411212316US

POWER SUPPLY REMOTE VOLTAGE SENSING

Christopher W. Dix

5

Technical Field

The present invention relates generally to electrical circuits and, more particularly, to power supplies.

Background

10 Power supplies (e.g., direct current (DC) converters) are typically employed for electrical devices, such as with integrated circuit applications, to provide the desired power supply voltages. For example, Fig. 1 shows a circuit 100 illustrating a conventional DC-to-DC power supply converter 102, 15 with input terminals 110 and output terminals 112, coupled to a load 104. Resistors 106 represent trace (e.g., lead or wire) and connector resistances (i.e., associated supply and return path resistances) between converter 102 and load 104.

As an example for low current load applications, remote 20 sensing of a voltage level at load 104 is not required, because resistors 106 are typically less than an ohm and internal feedback resistors 108, which may be 1,000 ohms or more, maintain a desired voltage level at output terminals 112 and at load 104. However, as the current through load 104 increases, 25 an undesired voltage drop will also increase across resistors 106, which may result in an under-voltage situation at load 104. For example, if a resistance of each resistor 106 is 0.01 ohms and a current level through load 104 is 10 amps, a voltage level at load 104 will be reduced by 0.20 volts. If the desired 30 voltage level at load 104 is 1.2 volts, the error (0.20 volts)

will exceed fifteen percent, which is generally too far out of tolerance for typical applications.

To compensate for this undesired voltage drop, remote sense paths 114 may be coupled between sense terminals 116 of converter 102 and load 104 to provide direct feedback from load 104 and allow converter 102 to actively maintain the desired voltage level at load 104 (i.e., via remote sensing). A power metal oxide semiconductor field effect transistor (MOSFET) may also be inserted between output terminal 112 (e.g., out+) of converter 102 and load 104 (i.e., the supply path) to control when load 104 receives power from converter 102. However, an on-resistance of the power MOSFET (e.g., 0.01 ohms) adds to the resistance of resistor 106 to further decrease a voltage level at load 104. Furthermore, by including the power MOSFET in the supply path, remote sensing becomes more complex and difficult.

For example, if remote sense paths 114 are connected between converter 102 and load 104 when the power MOSFET is switched off, the feedback voltage provided to converter 102 via remote sense paths 114 will be approximately zero volts and an output voltage of converter 102 may be unreliable. As an example, converter 102 may provide a maximum voltage output or a voltage output that oscillates wildly, which may result in damage to converter 102 or to surrounding circuitry. As a result, there is a need for improved power supply techniques.

SUMMARY

Systems and methods are disclosed herein to provide power supply systems for electrical devices and to provide remote voltage sensing techniques to assist in maintaining desired 5 voltage levels at a load. For example, in accordance with an embodiment of the present invention, a power MOSFET and a small signal level MOSFET are simultaneously controlled by a controller to control the application of power to a load and sense a voltage level at the load. The controller may control 10 the application of power to a number of loads simultaneously and may further provide voltage tracking for the loads.

More specifically, in accordance with one embodiment of the present invention, a circuit includes a first transistor adapted to connect an output terminal of a first power supply to a first 15 load; a second transistor adapted to connect a sense terminal of the first power supply to the first load; and a controller adapted to provide an output signal to the first transistor and the second transistor to control the first transistor and the second transistor.

20 In accordance with another embodiment of the present invention, a power supply system includes a power supply adapted to provide a first voltage; a load adapted to receive the first voltage from the power supply; a first transistor coupled to an output terminal of the power supply and to the load; a first 25 resistor coupled to a gate terminal of the first transistor; a second transistor coupled to a sense terminal of the power supply and to the load; and a second resistor coupled to a gate terminal of the second transistor, wherein a first control signal provided via the first resistor and the second resistor 30 controls the first transistor and the second transistor, respectively.

In accordance with another embodiment of the present invention, a method of providing remote voltage sensing at a load includes providing a supply voltage from a power supply to the load; and providing feedback of the supply voltage at the 5 load to the power supply, wherein the providing of the supply voltage and the providing of the feedback occurs approximately simultaneously via a first control signal.

In accordance with another embodiment of the present invention, a power supply control circuit includes a power 10 supply controller adapted to provide a control signal to control the application of a power supply voltage from a power supply to a load, the power supply controller having an output terminal adapted to provide the control signal; and a first transistor, under control of the power supply controller via the control 15 signal, adapted to connect a feedback path for remote sensing from the load to the power supply at approximately the same time as the application of the power supply voltage from the power supply to the load.

The scope of the invention is defined by the claims, which 20 are incorporated into this section by reference. A more complete understanding of embodiments of the present invention will be afforded to those skilled in the art, as well as a realization of additional advantages thereof, by a consideration of the following detailed description of one or more 25 embodiments. Reference will be made to the appended sheets of drawings that will first be described briefly.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 shows a circuit diagram illustrating a conventional 30 power supply system.

Fig. 2 shows a circuit diagram illustrating a power supply system in accordance with an embodiment of the present invention.

Fig. 3 shows a circuit diagram illustrating a power supply system in accordance with an embodiment of the present invention.

Embodiments of the present invention and their advantages are best understood by referring to the detailed description that follows. It should be appreciated that like reference numerals are used to identify like elements illustrated in one or more of the figures.

DETAILED DESCRIPTION

Fig. 2 shows a circuit 200 illustrating a power supply system in accordance with an embodiment of the present invention. Circuit 200 includes transistors 202 and 204, resistors 206 and 208, and a controller 210, in addition to converter 102 and load 104.

Transistor 202 (e.g., a power MOSFET) functions as a controllable switch for applying power from converter 102 to load 104 (i.e., selectively coupling output terminal (out+) 112 to load 104). Transistor 204 (e.g., a signal level or small signal level MOSFET) functions as a controllable switch for selectively coupling sense terminal (sense+) 116 to load 104. Transistors 202 and 204 may be switched on at approximately the same time because both are controlled by controller 210 (e.g., their gate terminals are biased from the same driver) and their source terminals have approximately the same return path (e.g., through load 104).

As discussed herein, resistors 106 represent associated path resistances. In circuit 200, resistors 106 associated with the supply path are split between converter 102 and transistor 202 and between transistor 202 and load 104 simply to illustrate 5 that there is a certain amount of resistance associated with the supply path on each side of transistor 202.

Resistors 206 and 208 are placed in the gate terminal paths of transistors 204 and 202, respectively. For example, resistor 208 may be placed in close physical proximity (e.g., as close as 10 possible) to the gate terminal of transistor 202 to minimize parasitic oscillations associated with transistor 202. A resistance value of resistor 208 may be, for example, 10 to 200 ohms.

Resistor 206 may be employed to fine-tune a turn-on time 15 constant of transistor 204, such as for example, to adjust for differences in threshold voltages and/or input capacitances of transistor 204 relative to transistor 202. For example, if transistor 204 is the same type of transistor as transistor 202 (e.g., both are power MOSFETs), a resistance value of resistor 20 206 may be, for example, approximately 100 ohms so that transistors 202 and 204 will switch on at approximately the same time. However, if transistor 204 is significantly smaller than transistor 202 (e.g., a signal level MOSFET versus a power MOSFET, respectively), transistor 204 will generally have a 25 lower input capacitance (e.g., gate-to-source and gate-to-drain capacitance) and a resistance value of resistor 206 may be, for example, 0.1 to 1 million ohms.

In general, a timing of the sense paths 212 connection 30 between converter 102 and load 104 must be synchronized to an application of power to load 104. For example, if the connection of remote sense paths 212 between converter 102 and load 104 is completed long after transistor 202 is switched on,

a voltage step may occur at load 104, which may be undesirable in certain applications. If the connection of remote sense paths 212 between converter 102 and load 104 is completed long before transistor 202 is switched on, an over-voltage or an 5 oscillation condition, as described previously, may occur.

By utilizing the techniques discussed in reference to circuit 200, sense terminal (sense+) 116 and output terminal (out+) 112 of converter 102 are coupled to load 104 at approximately the same time. Consequently, the remote voltage 10 sensing function occurs at approximately the same time that power is applied to load 104 such that a desired voltage level at load 104 may be applied and maintained due to the stable feedback via remote sense paths 212 to converter 102.

It should also be understood that numerous modifications 15 and variations are possible in accordance with the principles of the present invention. For example, although converter 102 is shown in an exemplary fashion as a DC-to-DC power converter, converter 102 may be any type of alternating current (AC) or DC power supply. Similarly, although transistors 202 and 204 are 20 shown and described as N-channel MOSFETs, other types of transistors may be utilized, depending upon the requirements and desired application. For example, P-channel MOSFETs may be implemented when providing negative voltages to load 104. Transistors 202 and 204 may be coupled to output terminal (out-) 25 112 and sense terminal (sense-) 116, respectively, depending upon a desired application, rather than coupled to output terminal (out+) 112 and sense terminal (sense+) 116, respectively, as shown in Fig. 2. Furthermore, load 104 is shown in an exemplary fashion as a resistor, but may be any type 30 of load, such as for example, an integrated circuit or other electrical device.

The various circuit elements shown in Fig. 2 may be discrete devices or one or more of the circuit elements may be combined in some fashion, for example, in accordance with one or more embodiments of the present invention. As an example,
5 converter 102, load 104, transistors 202 and 204, resistors 206 and 208, and controller 210 may be discrete devices. Alternatively, for example, transistor 204 and resistor 206 may be incorporated into controller 210 or load 104.

Controller 210 is employed to simultaneously control (i.e.,
10 switch on and off) transistors 202 and 204 (e.g., a power MOSFET and a small signal MOSFET, respectively) for power supply remote voltage application and sensing. Controller 210 may represent a current source, a driver, or any other type of conventional controller that can be configured to provide an output signal
15 (e.g., a current or a voltage).

Controller 210 may be programmable to permit a magnitude of its output signal or the rate at which the output signal increases or decreases (i.e., the ramp rate) to be varied. Alternatively, controller 210 may only provide a fixed voltage
20 or current or a fixed voltage or current increase and decrease pattern.

Controller 210 may also be utilized to control a number of power supplies. For example, Fig. 3 shows a circuit 300 illustrating a power supply system in accordance with an embodiment of the present invention. Controller 210 of circuit
25 300 controls transistors 202(1) and 204(1), 202(2) and 204(2), and 202(3) and 204(3) of associated converters 102(1) through 102(3) for applying power to and sensing a voltage level from corresponding loads 104(1) through 104(3).

30 Loads 104(1) through 104(3) may be separate electrical devices or represent one electrical device (e.g., an integrated

circuit such as central processing unit, a programmable logic device, or an application specific integrated circuit) requiring more than one power source or different power supply voltages.

For example, converters 102(1) through 102(3) may provide supply

5 voltages of 3.3, 2.5, and 1.5 volts, respectively, to corresponding loads 104(1) through 104(3) within an integrated circuit. Thus, controller 210 controls the application of these voltages to loads 104(1) through 104(3).

Controller 210 may also be employed to provide voltage
10 tracking (i.e., maintaining the voltages of multiple power supplies relative to each other when applying or removing power from their associated loads) for these power supplies (e.g., converters 102(1) through 102(3)). For example, controller 210 may control the application and/or removal of power from
15 converters 102(1) through 102(3) to loads 104(1) through 104(3) via associated transistors 202(1) through 202(3) and 204(1) through 204(3) to maintain the voltage levels applied to loads 104(1) through 104(3) relative to each other.

As an example, for an exemplary power-up procedure,
20 converters 102(1) through 102(3) bring their output voltages within regulation. Controller 210 then brings the gate terminal voltages of transistors 202(1) through 202(3) and 204(1) through 204(3) above a turn-on voltage threshold of these transistors in response to an internal or external triggering event (e.g., a
25 signal indicating that the output voltages of converters 102(1) through 102(3) are within regulation). As the gate terminal voltages of transistors 202(1) through 202(3) and 204(1) through 204(3) continue to increase under control of controller 210, the voltages applied to loads 104(1) through 104(3) will track one
30 another until they reach their desired respective supply voltage levels (e.g., 3.3, 2.5, and 1.5 volts, respectively).

An exemplary power-down procedure may be performed in a similar fashion with controller 210 decreasing the gate terminal voltages of transistors 202(1) through 202(3) and 204(1) through 204(3) and the voltages applied to loads 104(1) through 104(3)

5 tracking one another. Further details regarding voltage tracking may be found in U.S. Patent Application No. 10/408,720 entitled "Maintaining Voltage Tracking for DC Power Supplies" and filed April 7, 2003, which is incorporated herein by reference in its entirety.

10 The techniques discussed herein may be implemented, for example, to control the application of power to external loads or loads which are incorporated within the same electrical device (e.g., the same integrated circuit). Furthermore, one or more of the embodiments of the present invention may be
15 implemented to provide remote power (e.g., voltage level) sensing to, for example, compensate for a supply and a return path voltage drop and optionally control a voltage level ramp rate during the application of and/or removal from power to one or more loads for one or more power supplies.

20 In accordance with an embodiment of the present invention, techniques are disclosed for controlling two or more transistors (e.g., MOSFETs) simultaneously to connect the output pins and the sense pins from a power supply to a load. The transistors may be synchronized to the same drive signal, which may
25 eliminate a potential voltage step or undesired oscillation associated with conventional techniques. Furthermore, systems and methods discussed herein may greatly simplify the logic and control circuitry required to apply power to a circuit or implement power supply control as compared to conventional power
30 supply control systems which may add significantly to the design complexity and cost.

Embodiments described above illustrate but do not limit the invention. It should also be understood that numerous modifications and variations are possible in accordance with the principles of the present invention. Accordingly, the scope of
5 the invention is defined only by the following claims.